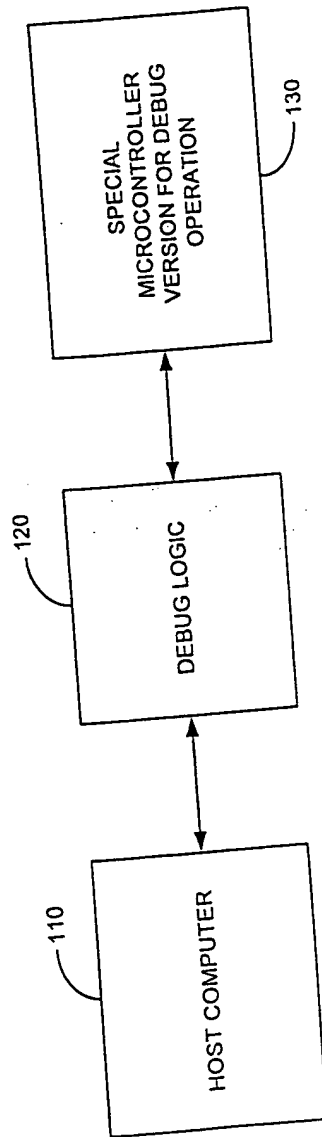


FIG. 1 is a block diagram of a system 100.



(PRIOR ART)

100

FIG. 1

FIG. 2 is a block diagram of a system 200 for controlling a production microcontroller 232 mounted in a POD 250. The system 200 includes a host computer 210, a virtual microcontroller 220, and a production microcontroller 232. The host computer 210 is connected to the virtual microcontroller 220 via a bidirectional data path 218. The virtual microcontroller 220 includes a memory 222 and four registers 221, 223, 224, and 225. The production microcontroller 232 includes four registers 236, 237, 238, and 239. The virtual microcontroller 220 and the production microcontroller 232 are connected via a data bus 227. The data bus 227 carries DATA CLOCK, μ C CLOCK, DATA 1, and DATA 2 signals. The production microcontroller 232 is mounted in a POD 250.

200

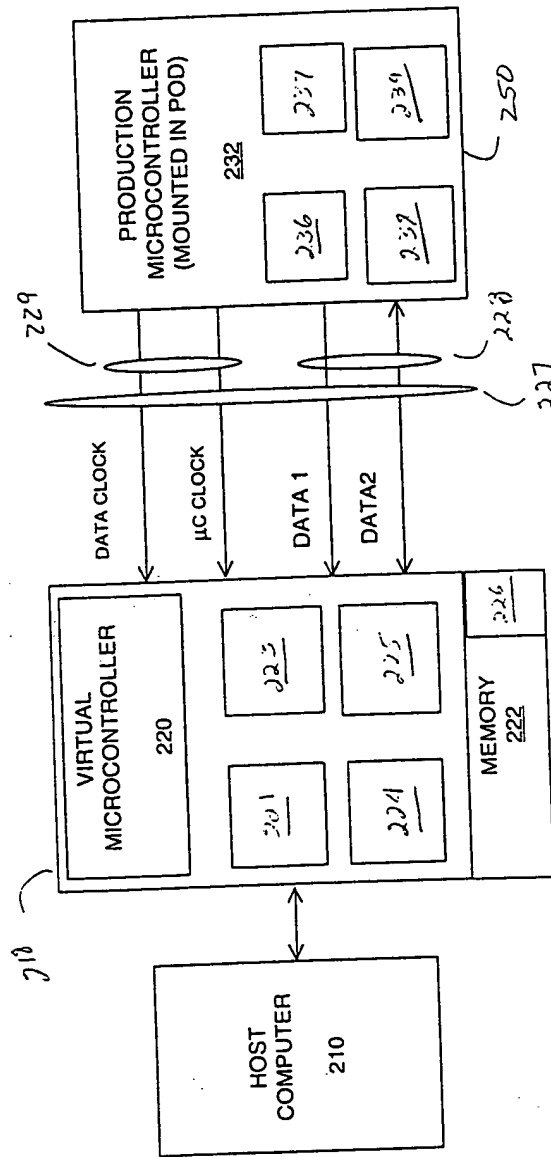


FIG 2

300

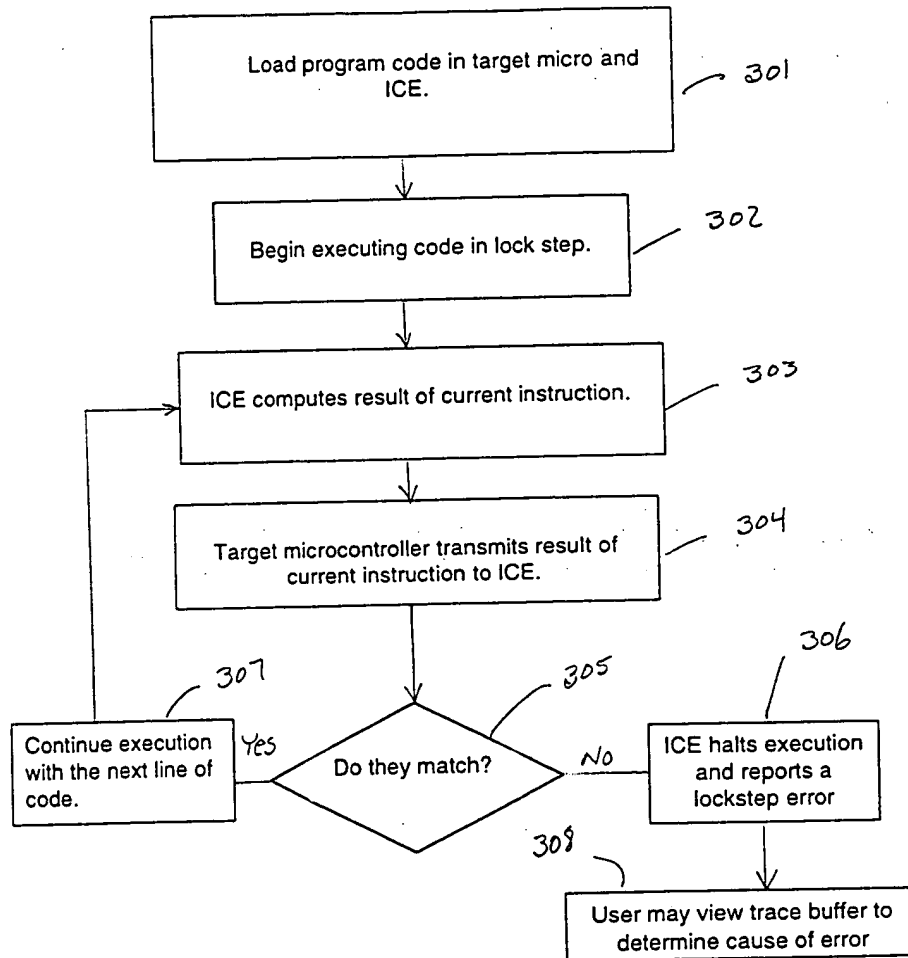


FIG. 3